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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/557,825	10/19/2006	Bernd Wunderer	WUND3005/JEK	6054
23364	7590	03/26/2008	EXAMINER	
BACON & THOMAS, PLLC 625 SLATERS LANE FOURTH FLOOR ALEXANDRIA, VA 22314			ELEY, JESSICA L	
		ART UNIT	PAPER NUMBER	
		2884		
		MAIL DATE		DELIVERY MODE
		03/26/2008		PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)	
	10/557,825	WUNDERER ET AL.	
	Examiner	Art Unit	
	JESSICA L. ELEY	2884	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 23 November 2005.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-10 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-10 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on 23 November 2005 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO/SB/08)
 Paper No(s)/Mail Date 23 November 2005, 19 October 2006.

4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
 5) Notice of Informal Patent Application
 6) Other: _____.

DETAILED ACTION

Specification

Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to *a single paragraph* on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

Claim Objections

Claim 6 is objected to under 37 CFR 1.75(c) as being in improper form because a multiple dependent claim 5. See MPEP § 608.01(n). Accordingly, claim 6 can not be further treated on the merits.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Nakai et al. US 6,094,281 (henceforth referred to as Nakai).

Regarding **claim 1**, Nakai teaches an apparatus for checking bank notes (column 1 lines 45-60) including a semiconductor array **1** which scans the bank notes to be checked, the semiconductor array being formed by at least two parallel spaced linear semiconductor arrays **2** and **3**, and wherein the bank notes are moved for the check past the semiconductor array and illuminated by a light source **205** (FIG. 11), comprising:

The linear semiconductor arrays are formed by at least three layers which are maximally sensitive to light of different wavelengths red (R), green (G), and blue (B), including a first linear semiconductor array **2** arranged to scan the bank notes in a defined range of sensitivity of the semiconductor, and a second linear semiconductor array **3** arranged to scan the bank notes in a sensitivity semiconductor array has a filter which passes only a part of the spectrum (column 7 lines 48-51).

Regarding **claim 2**, Nakai teaches an apparatus according to claim 1 above, wherein the first semiconductor array is sensitive to the total spectrum of visible light, and the second array is provided with a filter which passes only the invisible part of the spectrum (column 7 lines 48-51).

Regarding **claim 3**, Nakai teaches an apparatus according to claim 1 above, wherein the first semiconductor array **100** is sensitive to the total spectrum, and the second semiconductor array, **101** in corresponding FIG. 7, is provided with a filter which passes only the visible part of the spectrum but blocks the invisible part (FIG. 8).

Regarding **claim 4**, Nakai teaches an apparatus according to claim 1 above, wherein the first semiconductor array is provided with a filter which passes only the visible part of the spectrum (FIG. 2), and the second semiconductor array is provided with a filter which passes only an invisible part of the spectrum (FIG. 3).

Regarding **claims 5 and 6**, Nakai teaches an apparatus according to claims 2 to 4 above, wherein the invisible part of the spectrum is in the infrared range or the ultraviolet range (column 7 lines 33-37).

Regarding **claim 7**, Nakai teaches an apparatus according to claim 1 above, including a control and evaluation device **211** which is arranged to process and evaluate signals from the two semiconductor arrays in order to produce a three-color image (column 10 lines 25-28) and at least one image in the range of invisible light (column 10 lines 16-19) from the signal of the layer of the two linear semiconductor arrays by combination of the signal for each bank note to be checked (column 8 lines 62-63).

Regarding **claim 8**, Nakai teaches an apparatus according to claim 1 above, wherein the semiconductor array **1** and the light source **205** are disposed on the same side of the bank note (FIG. 11).

Regarding **claims 9 and 10**, Nakai teaches an apparatus according to claim 1 above, wherein the two linear semiconductor arrays are located on a the same silicon chip in a monolithic manner (column 19 lines 33-36).

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Jedlicka et al US 6,252,220 B1 teaches a sensor with multiple parallel linear arrays of selective translucent filtering layers mounted to the same substrate for full color scanning documents.

Oozu et al US 5,801,373 teaches a solid-state image pickup device that images visible as well as invisible light on a common substrate.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JESSICA L. ELEY whose telephone number is (571)272-9793. The examiner can normally be reached on Monday - Thursday 8:00-6:30 EST.

Art Unit: 2884

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dave Porta can be reached on (571) 272-2444. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

**/Constantine Hannaher/
Primary Examiner, Art Unit 2884**

/J. L. E./
Examiner, Art Unit 2884